## 2.9 Precharge-Evaluation logic.

The circuit is an N-type block. We have  $\overline{F} = S_n \Leftrightarrow F = \overline{S_n}$  $S_n = (b+c)a + bc = ab + ac + bc \Rightarrow F = \overline{S_n} = \overline{ab + ac + bc}$